

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6	tft with stack same thermal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 10:03
L2	17	tft same stack same thermal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 10:04
L3	0	thinned with chip with stack same thermal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 10:04
L4	1	thinned with chip same stack same thermal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 10:07
L5	4	((("4525921") or ("4646128") or ("4706166") or ("5104820")).PN.	USPAT	OR	OFF	2005/07/11 10:09
L6	1145	(257/686).CCLS.	USPAT	OR	OFF	2005/07/11 10:11
L7	0	("chipwithstackwiththermal").PN.	USPAT	OR	OFF	2005/07/11 10:11
L8	21	chip with stack with thermal with layer	USPAT	OR	ON	2005/07/11 10:12

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6	tft with stack same thermal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 10:03
L2	17	tft same stack same thermal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 10:04
L3	0	thinned with chip with stack same thermal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 10:04
L4	1	thinned with chip same stack same thermal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 10:07
L5	4	((("4525921") or ("4646128") or ("4706166") or ("5104820"))).PN.	USPAT	OR	OFF	2005/07/11 10:09
L6	1145	(257/686).CCLS.	USPAT	OR	OFF	2005/07/11 10:09

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	semicondcutor with thin with thermal same stack	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 08:57
L2	0	semicondcutor with thin with thermal same stack	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 08:57
L3	54	semiconductor with thin with thermal same stack	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 09:04
L4	1198	(257/712).CCLS.	USPAT	OR	OFF	2005/07/11 09:01
L5	890	(257/750).CCLS.	USPAT	OR	OFF	2005/07/11 09:02
L6	8	l4 and l5	USPAT	OR	OFF	2005/07/11 09:02
L7	193	semiconductor with thermal with stack	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 09:05
L8	117	semiconductor with thermal with stack and thin	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 09:07
L9	5	csp with thermal with stack and thin	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/07/11 09:07